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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10009910	FILING DATE 12/12/2001	CLASS 438	SUBCLASS 2812	GAU 2812	EXAMINER S079
<b>**APPLICANTS:</b> Iida Makoto; Kimura Masanori;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A 371 OF PCT/JP01/03101 04/10/2001					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2000-113297 04/14/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				81839.0107	
Verified and Acknowledged Examiners's initials					
<b>TITLE :</b> Silicon wafer, silicon epitaxial wafer, anneal wafer and method for producing them <small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-436L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	
		Total Claims	
		Print Claim for O.G.	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
		Print Fig.	
		Primary Examiner	
		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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